Agilent 75000 Series B

Agilent E1332A 4-Channel Counter/Totalizer Module

Service Manual

 Enclosed is the Service Manual for the Agilent E1332A 4-Channel Counter/Totalizer Module. Insert this manual, along with any other VXIbus manuals that you have, into the binder that came with your Agilent Technologies mainframe.

Manual Part Number: E1332-90013 Printed in Malaysia E0706

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> Agilent E1332A 4-Channel Counter/Totalizer Service Manual Edition 4 Rev 2

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Printing History

The Printing History shown below lists all Editions and Updates of this manual and the printing date(s). The first printing of the manual is Edition 1. The Edition number increments by 1 whenever the manual is revised. Updates, which are issued between Editions, contain replacement pages to correct the current Edition of the manual. Updates are numbered sequentially starting with Update 1. When a new Edition is created, it contains all the Update information for the previous Edition. Each new Edition or Update also includes a revised copy of this printing history page. Many product updates or revisions do not require manual changes and, conversely, manual corrections may be done without accompanying product changes. Therefore, do not expect a one-to-one correspondence between product updates and manual updates.

Safety Symbols

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Declares, that the product

Agilent Technologies

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Conforms with the following product standards:

1 June 2001

Date **Ray Corson** Product Regulations Program Manager

For further information, please contact your local Agilent Technologies sales office, agent or distributor. *Authorized EU-representative: Agilent Technologies Deutschland GmbH, Herrenberger Strabe 130, D 71034 Böblingen, Germany*

Manual Overview

This manual shows how to service the Agilent E1332A 4-Channel Counter/Totalizer. Consult the *Agilent E1332A User's Manual* for additional information on installing, configuring, and operating the counter. Consult the appropriate mainframe user's manual for information on configuring and operating the mainframe.

Manual Content

Contents

Agilent E1332A Counter/Totalizer Service Manual

Chapter 1 General Information

Introduction

This manual contains information required to test, adjust, troubleshoot, and repair the Agilent E1332A B-Size VXI Counter/Totalizer. See the *Agilent E1332A User's Manual* for additional information. Figure 1-1 shows the Agilent E1332A counter.

Figure 1-1. Agilent E1332A Counter/Totalizer

Safety Considerations

This product is a Safety Class I instrument that is provided with a protective earth terminal when installed in the mainframe. The mainframe, counter, and all related documentation should be reviewed for familiarization with safety markings and instructions before operation or service.

Refer to the WARNINGS on page 4 of this manual for a summary of safety information. Safety information for preventive maintenance, testing, adjusting, and service follows and is also found throughout this manual.

WARNINGS and CAUTIONS This section contains WARNINGS which must be followed for your protection and CAUTIONS which must be followed to avoid damage to the equipment when performing instrument maintenance or repair.

WARNING SERVICE-TRAINED PERSONNEL ONLY. The information in this manual is for service-trained personnel who are familiar with electronic circuitry and are aware of the hazards involved. To avoid personal injury or damage to the instrument, do not perform procedures in this manual or do any servicing unless you are qualified to do so.

> **CHECK MAINFRAME POWER SETTINGS. Before applying power, verify that the mainframe setting matches the line voltage and that the correct fuse is installed. An uninterruptible safety earth ground must be provided from the main power source to the mainframe input wiring terminals, power cord, or supplied power cord set.**

GROUNDING REQUIREMENTS. Interruption of the protective (grounding) conductor (inside or outside the mainframe) or disconnecting the protective earth terminal will cause a potential shock hazard that could result in personal injury. (Grounding one conductor of a two-conductor outlet is not sufficient protection.)

COMMON GROUND. Verify that a common ground exists between the unit under test and the counter (via the mainframe) prior to energizing either unit.

IMPAIRED PROTECTION. Whenever it is likely that instrument protection has been impaired, the mainframe must be made inoperative and be secured against any unintended operation.

REMOVE POWER IF POSSIBLE. Some procedures in this manual may be performed with power supplied to the mainframe while protective covers are removed. Energy available at many points may, if contacted, result in personal injury. (If maintenance can be performed without power applied, the power should be removed.)

USING AUTOTRANSFORMERS. If the mainframe is to be energized via an autotransformer (for voltage reduction) make sure the common terminal is connected to neutral (that is, the grounded side of the main's supply).

WARNING CAPACITOR VOLTAGES. Capacitors inside the mainframe may remain charged even when the mainframe has been disconnected from its source of supply. USE PROPER FUSES. For continued protection against fire hazard, replace the line fuses only with fuses of the same current rating and type (such as normal blow, time delay, etc.). Do not use repaired fuses or short-circuited fuseholders. CAUTION Static electricity is a major cause of component failure. To prevent damage to the electrical components in the counter, observe anti-static techniques whenever working on the counter.

Counter Description

Counter Options There are no electrical or mechanical options available for the Agilent E1332A counter. However, you can order Option 1BN which provides a MIL-STD-45662A Calibration Certificate, or Option 1BP which provides the Calibration Certificate and measurement data. Contact your nearest Agilent Technologies Sales and Service Office for information on Options 1BN and 1BP.

Recommended Test Equipment

Table 1-1 lists the test equipment recommended for testing, adjusting, and servicing the counter. Essential requirements for each piece of test equipment are described in the Requirements column.

Table 1-1. Recommended Test Equipment

Introduction

This chapter provides information for installing the Agilent E1332A counter, including:

- initial inspection
- preparation for use
- environment
- storage and shipping

Initial Inspection

Inspect the shipping container for damage. If the shipping container or cushioning material is damaged, keep the container until the shipment contents have been checked and the instrument has been checked mechanically and electrically. See Chapter 4 for procedures to check electrical performance.

WARNING To avoid possible hazardous electrical shock, do not perform electrical tests if there are signs of shipping damage to any portion of the outer enclosure (covers, panels, etc.).

> If the contents are incomplete, if there is mechanical damage or defect, or if the instrument does not pass the electrical performance tests, notify your nearest Agilent Technologies Sales and Service Office. If the shipping container is damaged or the cushioning material shows signs of stress, notify the carrier as well as Agilent Technologies, and keep the shipping materials for the carrier's inspection.

Preparation for Use

See Chapter 2 of the *Agilent E1332A User's Manual* to prepare the Agilent E1332A counter for use. See the appropriate mainframe user's manual to prepare your mainframe. If your mainframe is not manufactured by Agilent Technologies, consult the manufacturer for a list of available manuals.

The recommended operating environment for the Agilent E1332A counter is 0^oC to +55^oC with humidity <65% relative (0^oC to +40^oC). The instrument should be stored in a clean, dry environment. For storage and shipment, the temperature range is -40^oC to +75^oC, with humidity <65% relative (0° C to +40^oC).

Shipping the Counter

If you need to return the Agilent E1332A counter to Agilent Technologies, first remove any adapters or connectors before packaging the instrument for shipment. When you return the instrument to Agilent Technologies, attach a tag to the instrument identifying the owner and indicating service or repair required. In any correspondence, refer to the instrument by model number and full serial number.

When shipping the instrument, we recommend using containers and materials identical to those used in factory packaging, which are available through Agilent Technologies Sales and Service Offices. Mark the shipping container "FRAGILE" to assure careful handling.

If you use other (commercially available) shipping materials, wrap the instrument in heavy paper or plastic. Use a strong shipping container. A double-wall carton of 2.4 MPa (350 psi) test material is adequate.

Use enough shock-absorbing material (75 to 100 mm layer; 3 to 4 inches) around all sides of the instrument to provide a firm cushion and prevent movement in the container. Protect the front panel with cardboard. Seal the shipping container securely and mark the container "FRAGILE" to assure careful handling.

Introduction

This chapter lists operating information for the Agilent E1332A counter, including:

- Counter operation
- Preventive maintenance
- Operator's check (self-test)

Counter Operation

See the *Agilent E1332A User's Manual* for counter operation, including:

- Getting started
- Configuring the counter
- Using the counter
- Understanding the counter
- Counter command reference
- Counter specifications
- Counter error messages
- Register-based programming

Preventive Maintenance

Preventive maintenance for the Agilent E1332A counter consists of periodically cleaning the counter and then running the Operator's Check (***TST?** command). For best results, you should clean the counter once a year, or more often if the counter is used in a very dusty or very humid area. See Table 3-1 for recommended cleaning equipment and supplies.

Table 3-1. Recommended Cleaning Equipment

Figure 3-1. Cleaning the Agilent E1332A Counter

Operator's Check

The Operator's Check for the Agilent E1332A counter consists of sending the self-test (***TST?**) command and checking the response. The operator's check can be used at any time to verify that the counter is connected properly and is responding to the self-test command.

As required, see the mainframe user's manual for information on address selection. See the *Agilent E1332A User's Manual* for information on counter SCPI commands.

- **Self-Test Procedure** 1. Verify that the counter is properly installed in the mainframe and that the mainframe has passed its power-on test.
	- 2. Execute the counter functional test using the ***TST?** command (see example following).
	- 3. A "0" returned means no self-test failure, while any other number returned means a failure was detected. See Chapter 7, "Service," for troubleshooting information.
	- **NOTE** All pull-up jumpers must be in the "NO PULL-UP" position, or the counter will fail its self-test.

Example: Counter Self-Test

An example follows which uses an HP 9000 Series 300 computer with BASIC and a counter address of 70906.

- 10 OUTPUT 70906;"*TST?" *!Send the self-test command*
- 20 ENTER 70906;A *!Enter self-test result*
- 30 PRINT A
- 40 END

Introduction

The three levels of test procedures described in this chapter are used to verify that the Agilent E1332A Counter:

- is fully functional (Functional Verification)
- meets selected testable specifications (Operation Verification)
- meets all testable specifications (Performance Verification)

WARNING Do not perform any of the following verification tests unless you are a qualified, service-trained technician and have read the WARNINGS and CAUTIONS in Chapter 1.

Test Conditions and Procedures See Table 1-1 for test equipment requirements. If you are using an Agilent 3325 Function Generator (or another source that requires a 50 ohm load), you should connect a 50-ohm feed-through termination at the output of the source. Otherwise, the actual voltage output seen by the counter will be approximately twice as high as expected.

> For best accuracy, the ambient temperature of the test area should be between 18° C and 28° C and stable to within $\pm 1^{\circ}$ C. You should complete the Performance Verification tests at least once a year. For heavy use or severe operating environments, perform the tests more often.

The verification tests assume that the person performing the tests understands how to operate the mainframe, the counter, and specified test equipment. The test procedures do not specify equipment settings for test equipment, except in general terms. It is assumed a qualified, service-trained technician will select and connect the cables, adapters, and probes required for the test.

Performance Test Record The results of each Performance Verification test may be recorded in Table 4-1, "Agilent E1332A Performance Test Record." You may make a photocopy of this form, if desired.

Functional Verification Test

The procedures in this section are used to quickly verify that the counter is operating correctly. These tests are not required before performing Operation Verification or Performance Verification.

The recommended Functional Verification sequence includes the Self-test, the Isolated Inputs test, the Non-isolated Inputs test, and the Up/Down Counting test. Other tests included in this section are optional, and are intended primarily for troubleshooting.

- **Self-test** The purpose of this test is to verify communication with the mainframe, the external controller, and/or the external terminal by performing a self-test. The self-test also checks the counter's trigger level circuitry and performs an internal check of the frequency, period, and totalize functions.
- **Test Procedure** 1. Verify that the counter is correctly installed in the mainframe. No external signals should be connected to the inputs of the counter.
	- 2. On the mainframe, connect a power cable and set the power to ON. Verify that the mainframe performs a proper power-up sequence. See the mainframe user's manual for additional information.
	- 3. Execute the counter self-test using the ***TST?** command.

Figure 4-1. Isolated Inputs Test Setup

Test Procedure 1. Begin totalizing:

- **–** Channel 1 ... **INIT1**
- **–** Repeat for channels 2, 3, and 4
- 2. Execute the following commands in a continuous loop:

- 3. Verify that all channels are totalizing properly.
- 4. Set input frequency to 4 MHz. Verify that all channels are totalizing properly.

Example: Isolated

Inputs Test

Non-isolated Input Test

This test verifies the operation of all four non-isolated inputs. Each channel will totalize a 1 Hz signal and a 4 MHz signal.

- **Equipment Setup** 1. Connect the equipment as shown in Figure 4-2.
	- 2. Set the Function Generator controls as follows:

– Output .. 25 mV rms

Figure 4-2. Non-Isolated Inputs Test Setup

- 160 !
- 170 CLEAR SCREEN
- 180 END

Up/Down Count Test

This test verifies the up/down counting function on channel pairs 1/2 and 3/4. After the measurement is initiated:

- **–** the count will increment when only channel 1 (or only channel 3) is connected to the source
- **–** the count will not change when both channels of a channel pair are connected
- **–** the count will decrement when only channel 2 (or only channel 4) is connected to the source

- **Equipment Setup** 1. Connect the equipment as shown in Figure 4-3 (begin with only channel 1 connected to the source).
	- 2. Set the Function Generator controls as follows:

- **–** DC Offset ... 0 V
- **–** Output .. 100 mV rms

3. Set up the counter as follows:

– Channel pair 3/4 **CONF3:UDC**

Figure 4-3. Up/Down Counting Test Setup

- **Test Procedure** 1. Initiate measurements:
	- **–** Channel pair 1/2 .. **INIT1 –** Channel pair 3/4 .. **INIT3**
	- 2. Execute the following commands in a continuous loop:
		- **–** Channel pair 1/2 .. **FETC1?**
		- **–** Channel pair 3/4 .. **FETC3?**
	- 3. For channel pair 1/2, verify that the count increments when only channel 1 is connected to the source, decrements when only channel 2 is connected, and does not change when channels 1 and 2 are both connected.
	- 4. Repeat step 3 for channel pair 3/4, substituting channel 3 for channel 1, and channel 4 for channel 2.

Example: Up/Down Counting Test

Trigger Polarity Test (Optional)

This test verifies positive and negative trigger polarities on all four channels. The pull-up jumpers are used to increment each channel by one count at a time.

- **Equipment Setup** 1. Configure the counter as shown in Figure 4-4. Remove all four pull-up jumpers.
	- 2. Set up the Agilent E1332A as follows:
		- **–** Reset counter ... ***RST**
		- **–** Ch. 1/2 trig level to 2.5V **SENS1:EVEN:LEV 2.5**
		- **–** Ch. 3/4 trig level to 2.5V **SENS3:EVEN:LEV 2.5**
		- **–** Low-pass filter to ON **INP:FILT ON –** Low-pass filter to 4 Hz **INP:FILT:FREQ 4**
	- 3. Configure the Agilent E1332A to totalize on all four channels:
		- **–** Channel 1 to totalize **CONF1:TOT**
		- **–** Repeat for channels 2, 3, and 4

Figure 4-4. Trigger Polarity Test Setup

Test Procedure 1. Begin totalizing:

- - **–** Channel 1 ...**. INIT1**
	- **–** Repeat for channels 2, 3, and 4
- 2. Execute the following commands in a continuous loop:
	- **–** Channel 1 ... **FETC1? –** Channel 2 ... **FETC2? –** Channel 3 ... **FETC3?**
	- **–** Channel 4 ... **FETC4?**
- 3. Install (in the left-most pins) and remove pull-up jumper 1PU several times, verifying that the count increments when the jumper is installed, and does not increment when the jumper is removed.
- 4. Repeat for channel 2 (jumper 2PU), channel 3 (jumper 3PU), and channel 4 (jumper 4PU).
- 5. Set all four channels to trigger on the negative edge:
	- **–** Channel 1 **SENS1:EVEN:SLOP NEG**
	- **–** Repeat for channels 2, 3, and 4
- 6. Repeat steps 1 through 4. This time, verify that the count increments when the jumper is removed, and does not increment when the jumper is installed.

Example: Trigger Polarity Test

Gated Totalizing Test (Optional)

This test verifies the gated totalizing function on both channel pairs. The pull-up jumpers for channels 2 and 4 are used as gates for channels 1 and 3, respectively. Positive and negative gate polarities are tested.

Equipment Setup 1. Connect the equipment as shown in Figure 4-5. Remove the pull-up jumpers for channels 2 and 4.

Figure 4-5. Gated Totalizing Test Setup

2. Set the Function Generator controls as follows:

NOTE These settings produce a square wave with lower edge at 0 V and upper edge at $+5$ V.

3. Set up the Agilent E1332A as follows:

- **–** Channel 3 to totalize **CONF3:TOT**
- **–** Channel 2 gate to ON **SENS2:TOT:GATE ON**
- **–** Channel 4 gate to ON **SENS4:TOT:GATE ON**

Test Procedure 1. Begin totalizing:

- **–** Channel pair 1/2 .. **INIT1 –** Channel pair 3/4 .. **INIT3**
- 2. Execute the following commands in a continuous loop:

- 3. Install (in the left-most pins) and remove pull-up jumper 2PU several times, verifying that the channel 1 count increments when the jumper is installed, and does not increment when the jumper is removed.
- 4. Repeat for channel pair 3/4, substituting channel 3 for channel 1, and channel 4 for channel 2.
- 5. Set the gate polarities to negative:
	- **–** Channel 2 **SENS2:TOT:GATE:POL INV**
	- **–** Channel 4 **SENS4:TOT:GATE:POL INV**
- 6. Repeat steps 1 through 4. This time, verify that the count increments when the corresponding jumper is removed, and does not increment when the jumper is installed.

Example: Gated Totalizing Test

- 3. Set up the Agilent E1332A as follows:
	- **–** Reset counter ... ***RST**
	- **–** Low-pass filter to ON **INP:FILT ON**
- 4. Configure the Agilent E1332A to totalize on all four channels:
	- **–** Channel 1 to totalize **CONF1:TOT**
	- **–** Repeat for channels 2, 3, and 4.

Figure 4-6. Input Filter Test Setup

4. Verify that all four channels are incrementing.
- 5. Change input frequency to 6.82 Hz (reject frequency).
- 6. Verify that all four channels are not incrementing.
- 7. Repeat steps 1 through 6 using the frequencies in the following table.

Example: Input Filter Test

Trigger Level Test (Optional)

This test verifies trigger level operation for all four channels. Four trigger levels will be tested.

- **Equipment Setup** 1. Connect the equipment as shown in Figure 4-7.
	- 2. Set up the Agilent E1332A as follows:
		- **–** Reset counter ... ***RST**
		- **–** Low-pass filter to ON **INP:FILT ON**
		- **–** Low-pass filter to 4 Hz **INP:FILT:FREQ 4**
	- 3. Configure the Agilent E1332A to totalize on all four channels:
		- **–** Channel 1 ... **CONF1:TOT**
		- **–** Repeat for channels 2, 3, and 4

Figure 4-7. Trigger Level Test Setup

- **Test Procedure** 1. Set Agilent E1332A trigger levels:
	- **–** Ch. 1/2 trig lev to -2.56V **SENS1:EVEN:LEV -2.56**
	- **–** Ch. 3/4 trig lev to -2.56V..... **SENS3:EVEN:LEV -2.56**
	- 2. Begin totalizing:
		- **–** Channel 1 .. **INIT1**
		- **–** Repeat for channels 2, 3, and 4
	- 3. Execute the following commands in a continuous loop:

- 4. Set the DC Supply to -2.84 Vdc.
- 5. Increase the supply voltage to -2.28 Vdc. Verify that all four channels increment. Incrementing by more than one count is not a problem.
- 6. Repeat steps 1 through 5 using the values in the following table:

7. Set all four input level jumpers to the /10 position (right-most pins). This must be completed before continuing, because the higher voltages used in the following steps will damage the inputs at their normal level.

CAUTION This must be completed before continuing, because the higher voltages used in the following steps will damage the inputs at their normal level.

- 8. Set trigger levels to $+2.54$ V. With the input level at $/10$, this causes the counter to trigger at $+25.4$ V.
	- **–** Ch. 1/2 trig level **SENS1:EVEN:LEV 2.54**
	- **–** Ch. 3/4 trig level **SENS3:EVEN:LEV 2.54**
- 9. Repeat steps 2 through 5, using 22.6 Vdc as the low voltage and 28.2 Vdc as the high voltage.
- 10. Set the supply voltage to 0 V, THEN place the input level jumpers in the normal position (left-most pins). Do not move the input level jumpers while the inputs are connected to >5 V.

Example: Trigger Level Test

Operation Verification

The procedures in this section are used to provide a high confidence that the counter is meeting published specifications. The Operation Verification tests are a subset of the Performance Verification tests and are suitable for checkout after performing repairs.

Operation Verification is performed by completing the following portions of the Performance Verification tests:

Performance Verification

The procedures in this section are used to test the counter's electrical performance using the specifications in Appendix A of the *Agilent E1332A User's Manual* as the performance standards. These tests are suitable for incoming inspection, troubleshooting, and preventive maintenance.

Test 4-1: Frequency Test

This test verifies frequency measurement accuracy at 16 different gate (aperture) times. Input level sensitivity is tested indirectly by using input signals with amplitudes equal to the sensitivity limits. Channel 1 and channel 3 are tested.

- **Equipment Setup** 1. Connect the equipment as shown in Figure 4-8.
	- 2. Set the Function Generator controls as follows:
		- **–** Function .. Sinewave **–** Frequency .. 4 MHz **–** DC Offset .. 0 V **–** Output .. 50 mV rms
	- 3. Set up the Agilent E1332A as follows:
		- **–** Reset counter ... ***RST**
		- **–** Bus is trigger source **TRIG:SOUR BUS**
		- **–** Channel 1 to frequency **SENS1:FUNC:FREQ**
		- **–** Channel 3 to frequency **SENS3:FUNC:FREQ**

Figure 4-8. Frequency Test Setup

Test Procedure 1. Set the gate time:

- - **–** Ch. 1 gate time to 2 ms **SENS1:FREQ:APER .002**
	- **–** Ch. 3 gate time to 2 ms **SENS3:FREQ:APER .002**
- 2. Perform measurement:
	- **–** Initiate measurement on channel 1 **INIT1**
	- **–** Initiate measurement on channel 3 **INIT3**
	- **–** Trigger measurements ***TRG**
	- **–** Return channel 1 measurement results **FETC1?**
	- **–** Return channel 3 measurement results **FETC3?**
- 3. Repeat steps 1 and 2 using the values in the following table. Verify that the results are within the limits specified in Table 4-1, "Agilent E1332A Performance Test Record. "

Example: Frequency

Test

Test 4-2: Period Average Test

This test verifies period measurement accuracy at 7 different sampling times (i.e., number of periods averaged). Channel 1 and channel 3 are tested.

Equipment Setup 1. Connect the equipment as shown in Figure 4-9.

2. Set the Function Generator controls as follows:

- **–** Output .. 50 mV rms
- 3. Set up the Agilent E1332A as follows:
	- **–** Reset counter ... ***RST**
	- **–** Bus is trigger source **TRIG:SOUR BUS**
	- **–** Channel 1 to period **SENS1:FUNC:PER**
	- **–** Channel 3 to period **SENS3:FUNC:PER**

Figure 4-9. Period Average Test Setup

- **Test Procedure** 1. Set the sampling time (number of periods averaged):
	- **–** Ch. 1 to 1024 periods **SENS1:PER:NPER 1024 –** Ch. 3 to 1024 periods **SENS3:PER:NPER 1024**

2. Perform measurements:

- **–** Initiate measurement on channel 1 **INIT1**
- **–** Initiate measurement on channel 3 **INIT3**
- **–** Trigger measurements**. *TRG**
- **–** Return channel 1 measurement results **FETC1?**
- **–** Return channel 3 measurement results **FETC3?**

3. Repeat steps 1 and 2 using the values in the following table. Verify that the results are within the limits specified in Table 4-1, "Agilent E1332A Performance Test Record."

Example: Period Average Test

Test 4-3: Pulse Width Test

This test verifies pulse width measurement accuracy on channel 2 and channel 4.

- **Equipment Setup** 1. Connect the equipment as shown in Figure 4-10.
	- 2. Set the Function Generator controls as follows:

- 3. Set up the Agilent E1332A as follows:
	- **–** Reset counter ... ***RST**

- **Test Procedure** 1. Measure positive and negative pulse widths on channels 2 and 4:
	- **–** Channel 2 positive pulse width **MEAS2:PWID?**
	- **–** Channel 2 negative pulse width **MEAS2:NWID?**
	- **–** Channel 4 positive pulse width **MEAS4:PWID?**
	- **–** Channel 4 negative pulse width **MEAS4:NWID?**
	- 2. Verify that the results are within the limits specified in Table 4-1, "Agilent E1332A Performance Test Record."

Example: Pulse Width

Test

Test 4-4: Time Interval Test

The purpose of this test is to verify time interval measurement accuracy. The time intervals between channel 1 rising edge and channel 2 falling edge, and between channel 3 rising edge and channel 4 falling edge, are measured for a 1 MHz signal.

- **Equipment Setup** 1. Connect the equipment as shown in Figure 4-11.
	- 2. Set the Function Generator controls as follows:

NOTE These settings produce a square wave with lower edge at 0 V and upper edge at $+5$ V.

- 3. Set up the Agilent E1332A as follows:
	- **–** Reset counter ... ***RST**
	- **–** Ch. 1/2 trig level **SENS1:EVEN:LEV 2.5**
	- **–** Ch. 3/4 trig level **SENS3:EVEN:LEV 2.5**
	- **–** Ch. 2 event slope **SENS2:EVEN:SLOP NEG**
	- **–** Ch. 4 event slope **SENS4:EVEN:SLOP NEG**

Figure 4-11. Time Interval Test Setup

- **Test Procedure** 1. Measure time interval for channel pairs $1/2$ and $3/4$:
	- **–** Channel 1/2 time interval **MEAS1:TINT?**
	- **–** Channel 3/4 time interval **MEAS3:TINT?**
	- 2. Verify that the results are within the limits specified in Table 4-1, "Agilent E1332A Performance Test Record."

Example: Time Interval Test

Performance Test Record

Table 4-1. Performance Test Record for the Agilent E1332A Counter (Page 1 of 5)

Table 4-1. Performance Test Record for the Agilent E1332A Counter (Page 2 of 5)

Report No.

Date $\overline{}$

Table 4-1. Performance Test Record for the Agilent E1332A Counter/Totalizer (Page 3 of 5)

* Measurement Uncertainty of an Agilent 3325B, locked to a house frequency standard with accuracy of ±3.0E-11. (Accuracy must be greater than $\pm 6.0E$ -7 for all TAR's to be >10:1.)

** TAR = Counter Accuracy/Measurement Uncertainty.

* Measurement Uncertainty of an Agilent 3325B, locked to a house frequency standard with accuracy of ±3.0E-11. (Accuracy must be greater than ±6.0E-7 for all TAR's to be >10:1.)

** TAR = Counter Accuracy/Measurement Uncertainty.

* Measurement Uncertainty of an Agilent 3325B, locked to a house frequency standard with accuracy of ±3.0E-11. (Accuracy must be greater than ±6.0E-7 for all TAR's to be >10:1.)

** TAR = Counter Accuracy/Measurement Uncertainty.

Introduction

This chapter contains procedures to adjust the Agilent E1332A counter for peak performance. For best performance, the instrument should be adjusted after repair. The following adjustments are available:

- **–** 10 MHz reference oscillator
- **–** Channel pair 1/2 trigger level zero
- **–** Channel pair 3/4 trigger level zero

All adjustments are made manually, so the Agilent E1332A card must be accessible to the technician. Install the counter in an Agilent E1400T mainframe (which has no top or sides) if one is available. Otherwise, install the counter in the lowest slot of the B-size mainframe, and remove any cards above the counter.

WARNING Do not perform any of the following adjustments unless you are a qualified, service-trained technician, and have read the WARNINGS and CAUTIONS in Chapter 1.

Adjustment Conditions and Procedures

For best accuracy, the temperature of the area where adjustments are made should be between 18° C and 28° C and stable to within $\pm 1^{\circ}$ C. See Table 1-1, "Recommended Test Equipment," for test equipment requirements.

The adjustment procedures assume that the person performing the adjustments understands how to operate the mainframe, counter, and specified test equipment. The adjustment procedures do not specify test equipment settings, except in general terms. It is assumed that a qualified, service-trained technician will select and connect the cables and jumpers required for the adjustments.

Reference Oscillator Adjustment

This procedure adjusts the 10 MHz reference oscillator. For best results, use a universal counter that has an accuracy of at least ±0.1 ppm.

- **Equipment Setup** 1. Connect the equipment as shown in Figure 5-1. If necessary, consult Figure 5-4 for component locations.
	- 2. Set the universal counter to measure frequency.

Figure 5-1. Reference Oscillator Adjustment Setup

Adjustment Procedure 1. Adjust U42 until the universal counter reads 10 MHz \pm 10 Hz. This will give the reference oscillator an accuracy of ± 1 ppm or better.

Trigger Level Zero Adjustment

Channel Pair 1/2 This procedure adjusts the trigger level zero for channel pair 1/2.

Equipment Setup 1. Connect the equipment as shown in Figure 5-2. If necessary, consult Figure 5-4 for component locations.

2. Set the function generator controls as follows:

3. Ensure that the counter is in its power-on state by cycling power or executing the ***RST** command.

- **Adjustment Procedure** 1. The triangle wave input to channel 1 should produce a squarewave output at U44, pin 7 (referenced to the input common). Set the oscilloscope to view one cycle of the squarewave on the display (10 µsec/div for a 10 KHz signal).
	- 2. Adjust R33 until the positive and negative portions of the squarewave are equal in width.
- 3. Reduce the function generator output to 250 mV p-p. Repeat step 2.
- 4. Reduce the function generator output to 100 mV p-p. Repeat step 2. The adjustment accuracy increases for lower triangle wave amplitudes.

NOTE Triangle wave amplitudes below 100 mV p-p begin to approach the input level sensitivity limits of the counter, causing the adjustment to become invalid.

Channel Pair 3/4 This procedure adjusts the trigger level zero for channel pair 3/4.

Equipment Setup 1. Connect the equipment as shown in Figure 5-3. If necessary, consult Figure 5-4 for component locations.

Figure 5-3. Ch. 3/4 Trigger Level Zero Adjustment

2. Set the function generator controls as follows:

3. Ensure that the counter is in its power-on state by cycling power or executing the ***RST** command.

- **Adjustment Procedure** 1. The triangle wave input to channel 3 should produce a squarewave output at U43, pin 7 (referenced to the input common). Set the Oscilloscope to view one cycle of the squarewave on the display (10 µsec/div for a 10 KHz signal).
	- 2. Adjust R34 until the positive and negative portions of the squarewave are equal in width.
	- 3. Lower the Function Generator output to 250 mV p-p. Repeat step 2.
	- 4. Lower the Function Generator output to 100 mV p-p. Repeat step 2. The adjustment accuracy increases for lower triangle wave amplitudes.

NOTE Triangle wave amplitudes below 100 mV p-p begin to approach the input level sensitivity limits of the counter, causing the adjustment to become invalid.

Introduction

Replaceable Parts List

Table 6-1 below lists the replaceable parts for the Agilent E1332A counter. See Figure 6-1 later in this chapter for locations of parts listed in Table 6-1.

Table 6-1. Agilent E1332A Replaceable Parts

* See Table 6-2 for Reference Designator definitions

[a] Repair limited to replacement of parts listed - see Introduction for ordering information

 † These parts are not compatible with older version fixed handles and their corresponding front panels. To replace one or more of these older parts, you must order all three new parts (Top and Bottom Handle Kits AND External Panel).

Table 6-3. Agilent E1332A Reference Designators

Agilent E1332A REFERENCE DESIGNATORS

Figure 6-1. Agilent E1332A Replaceable Parts

Introduction

This chapter contains service information for the Agilent E1332A counter, including troubleshooting guidelines and repair/maintenance guidelines.

WARNING Do not perform any of the service procedures shown unless you are a qualified, service-trained technician, and have read the WARNINGS and CAUTIONS in Chapter 1.

Equipment Required Equipment required for counter troubleshooting and repair is listed in Table 1-1, "Recommended Test Equipment." Any equipment that satisfies the requirements given in the table may be substituted. To avoid damage to the screw head slots, use a T8 Torx driver to remove the front panel handles.

Service Aids Service aids on printed circuit boards include pin numbers, some reference designations, and assembly part numbers. See Chapter 6, "Replaceable Parts," for descriptions and locations of Agilent E1332A replaceable parts. Service notes, manual updates, and service literature for the Agilent E1332A counter may be available through Agilent Technologies. For information, contact your nearest Agilent Technologies Sales and Service Office.

Troubleshooting Techniques

There are two main steps in troubleshooting an Agilent E1332A counter problem: (1) identifying the problem, and (2) isolating the cause to a user-replaceable component.

Identifying the Problem

Counter problems can be divided into four general categories:

- Self-test errors
- Operator errors
- Catastrophic failures
- Performance out of specification

Self-Test Errors

A non-zero error number is returned when the counter self-test fails. If a self-test error occurs, cycle power and repeat the self-test. If the error repeats, see "Testing the Assembly" to troubleshoot the counter.

Operator Errors

Apparent failures may result from operator errors. See Appendix B, "Error Messages," in the *Agilent E1332A User's Manual* for information on operator errors.

Catastrophic Failure

If a catastrophic failure occurs, see "Testing the Assembly" to troubleshoot the counter.

Performance Out of Specification

If the counter performance is out of specification limits, use the adjustment procedures in Chapter 5 to correct the problem. If the condition persists, see "Testing the Assembly" to troubleshoot the counter.

Testing the Assembly You can use the tests and checks in Table 7-3 to isolate the problem to a user-replaceable part on the counter. See Figure 6-1 in Chapter 6 for locations of user-replaceable parts.

NOTE If the problem cannot be traced to a user-replaceable part listed in Table 6-1, return the counter to Agilent Technologies for exchange. See Chapter 6 for procedures.

Table 7-1. Agilent E1332A Tests/Checks

Checking for Heat Damage

Inspect the counter for signs of abnormal internally generated heat such as discolored printed circuit boards or components, damaged insulation, or evidence of arcing. If there is damage, do not operate the counter until you have corrected the problem.

Checking Switches/Jumpers

Verify that the logical address setting is set correctly (factory set at 48). Verify that the interrupt priority jumpers are set correctly (factory set at level 1). See the *Agilent E1332A User's Manual* for information.

Repair/Maintenance Guidelines

This section provides guidelines for repairing and maintaining the Agilent E1332A counter, including:

- ESD precautions
- Soldering printed circuit boards
- Post-repair safety checks
- **ESD Precautions** Electrostatic discharge (ESD) may damage MOS, CMOS, and other static sensitive devices in the Agilent E1332A counter. This damage can range from slight parameter degradation to catastrophic failure. When handling counter assemblies, follow these guidelines to avoid damaging counter components:
	- Always use a static-free work station with a pad of conductive rubber or similar material when handling counter components.
	- If a device requires soldering, be sure the assembly is placed on a pad of conductive material. Also, be sure that you, the pad, and the soldering iron tip are grounded to the assembly.

Soldering Printed Circuit Boards

The etched circuit boards in the counter have plated through-holes that allow a solder path to both sides of the insulating material. Soldering can be done from either side of the board with equally good results. When soldering to any circuit board, keep in mind the following guidelines:

- Avoid unnecessary component unsoldering and soldering. Excessive replacement can result in damage to the circuit board and/or adjacent components.
- Do not use a high power soldering iron on etched circuit boards, as excessive heat may lift a conductor or damage the board.
- Use a suction device or wooden toothpick to remove solder from component mounting holes. When using a suction device, be sure the equipment is properly grounded to prevent electrostatic discharge from damaging CMOS devices.

Post-Repair Safety Checks

After making repairs to the Agilent E1332A counter, inspect the counter for any signs of abnormal internally generated heat, such as discolored printed circuit boards or components, damaged insulation, or evidence of arcing. Determine and correct the cause of the condition. Then run the self-test (***TST?** command) to verify that the counter is functional.

Introduction

This appendix shows how counter accuracy is defined and calculated for the Agilent E1332A 4-Channel Counter/Totalizer. See Table 4-1, "Performance Test Record for the E1332A Counter," for values of counter accuracy.

Counter accuracy is defined as the expected accuracy of the measurement considering ONLY the Agilent E1332A counter. The "Low Limit" entry in Table 4-1 corresponds to the lower (-) value of counter accuracy, while the "High Limit" entry in Table 4-1 corresponds to the upper $(+)$ value of counter accuracy.

For further information on counter accuracy specifications, see Application Note 200, "Fundamentals of Electronic Counters" (Agilent part number 02-5952-7506) and Application Note 200-4, "Understanding Frequency Counter Specifications" (Agilent part number 02-5952-7522).

Calculating Counter Accuracy

For the Agilent E1332A counter, accuracy is defined for Frequency, Period Average, Pulse Width, and Time Interval Measurements by the following equation:

 $\textbf{accuracy} = \pm$ [resolution + timebase error + trigger noise error]

Resolution is defined as the smallest change in the measurement that can be detected. For frequency measurements, resolution is in Hz. For period average, pulse width, and time interval measurements, resolution is in seconds (see Table A-1).

Table A-1. Agilent E1332A Counter Resolution Equations

Measurement	Resolution	Range/values
Frequency	(Hz) gate time	.002, .004,,65.536 sec
Period Average	200 (nsec) # periods avgd	2, 4, 8, , 65536 periods
Pulse Width/ Time Interval	200 (nsec)	period

Timebase error is defined as the maximum fractional change in the 10 MHz reference timebase frequency due to all error sources, multiplied by the measurement result. That is:

timebase error = \pm [initial accuracy + aging rate + temperature drift] x measurement result.

For the Agilent E1332A, the *worst-case* values are:

- initial accuracy = $\pm 2 \times 10^{-6}$
- aging rate = $\pm 2 \times 10^{-6}$ /year
- temperature drift = \pm 5×10⁻⁶, 0^oC to 50^oC

However, typical maximum temperature drift is about ± 2 ppm (see Figure A-1), and calibration is usually performed at 1-year intervals. Thus, a typical timebase error = $\pm (2 \times 10^{-6} + 2 \times 10^{-6} + 2 \times 10^{-6}) = \pm (6 \times 10^{-6})$ x measurement result.

Trigger Noise Error Trigger noise error is defined as the additional error caused by counter input noise (200 μ V for the Agilent E1332A) and by noise on the input signal. The *input slew rate at trigger point* (in µV/sec) is the rate at which the input voltage is changing when the input is triggered. That is:

$$
slew rate = \frac{\Delta V}{\Delta t}
$$

For example, for a 50 mV square wave input with a 20 nsec rise time,

$$
\text{skew rate} = \frac{0.8 \times 50 \times 10^{-3}}{20 \times 10^{-9}} = 2 \times 10^{6} \text{ V/sec} = 2 \times 10^{12} \text{ }\mu\text{V/sec}
$$

TEMPERATURE IN DEGREES CELCIUS

Figure A-1.Typical Temperature Drift

Frequency Measurements Trigger Noise Error

From Appendix A of the *Agilent E1332A User's Manual*, for frequency measurements:

Trigger Noise Error (RMS) = T, where:

$$
T = \sqrt{\frac{(200 \mu V)^{2} + (e_n)^{2}}{input \,slew \,rate \,at \,trigger \,point}}
$$

 e_n = rms noise (in μ V) on the input signal for a 150 MHz bandwidth.

However, T is NOT the "trigger noise error" term for frequency measurements accuracy, but is only part of the expression. From Application Note 200-4, for frequency measurements:

trigger noise error (Hz)

$$
= [(1.4 \times T)/gate \text{ time}] \times frequency
$$

= 1.4 × $\sqrt{\frac{(200 \text{ }\mu \text{ V})^2 + (e_n)^2}{input \text{ slow rate at trigger point} \times gate \text{ time}}}$ × frequency

Period Measurements Trigger Noise Error

From Appendix A of the *Agilent E1332A User's Manual*, for period, pulse width, and time interval measurements:

Trigger Noise Error (RMS) $=$ T, where T is:

$$
T = \sqrt{\frac{(200 \mu V)^{2} + (e_n)^{2}}{input \, slow \, rate \, at \, trigger \, point}}
$$

However as with frequency measurements, T is NOT the "trigger noise error" term for period measurements, but is only part of the expression. For period average measurements, **trigger noise error** = $(1.4 \times T)/nper$, where *nper* = number of periods averaged. Therefore:

For pulse width and time interval measurements, multiple period averaging is not allowed, so **trigger noise error** $= (1.4 \times T)$. Therefore:

Counter Accuracy Equations Table

Table A-2 summarizes counter accuracy expressions for frequency, period average, pulse width, and time interval measurements. For any listed measurement, $accuracy = \pm$ [resolution + timebase error + trigger noise error].

Measurement	± resolution	ttimebase error*	±trigger noise error**
Frequency	(Hz) gate time $(.002, .004,,65.536$ sec)	timebase \times frequency (Hz)	$\frac{1.4 \times T}{gate \, time} \times frequency \, (Hz)$
Period Average	200 (nsec) $# periodsavgd$ (nsec) (2, 4, 8,,65536 periods)	timebase \times period (sec)	$\frac{1.4\times T}{\#periodsavgd}$ (sec)
Pulse Width	200 (nsec)	timebase \times pulse width (sec)	$1.4 \times T$
Time Interval	Same as Pulse Width	timebase \times interval (sec)	Same as Pulse Width

Table A-2. Agilent E1332A Counter Accuracy Equations

* timebase = [initial accuracy +aging rate + temp drift] = 6.0×10^{-6} (typical)

** T = $\sqrt{(200 \mu V)^2 + (e_{n})^2}$ input slew rate at triggerpoint

Accuracy Calculations Examples

Two examples follow to calculate Agilent E1332A counter accuracy. The first example calculates frequency measurement accuracy, while the second example calculates period measurement accuracy.

For frequency measurements: $accuracy(Hz) = \pm [resolution + timebase$ error + trigger noise error].

Calculate Resolution

For a gate time of 4096 msec, **resolution** = \pm 1/gate time $= \pm 1/(4.096) = \pm 0.244$ Hz

Calculate Timebase Error

For frequency measurements, **timebase error** = timebase x frequency $= \pm$ [initial accuracy + aging rate + temp drift] x frequency $= \pm (6.0 \times 10^{-6}) \times 10^{4}$ Hz $= \pm 0.06$ Hz

Calculate Trigger Noise Error

From Table A-2, for frequency measurements:

trigger noise error (Hz) = $\pm \frac{1.4 \times T}{gate \ time} \times frequency$

where $T = \sqrt{(200 \mu V)^2 + (e_n)^2}$ *input slew rate at trigger point*

 e_n = rms noise (in μ V) on the input signal

Thus, for input noise $e_n = 1$ mV, gate time of 4.096 sec, and slew rate of 1000 V/sec, the trigger noise error is:

trigger noise error (Hz)

$$
= \pm 1.4 \times \sqrt{\frac{(200 \text{ }\mu \text{ V})^2 + (1 \text{ } m\text{V})^2}{4.096 \text{ sec} \times 1000 \text{ V}_{sec}}} \times 10^4 \text{ Hz} = \pm 0.00342 \text{ Hz}
$$

Calculate Frequency Measurement Accuracy

Since **accuracy** (frequency measurements) $= \pm$ [resolution + timebase error + trigger noise error]

accuracy = \pm (0.2441 Hz + 0.06 Hz + 0.00342 Hz) $= \pm 0.3075$ Hz.

Effects of Varying Signal Conditions

Although this example showed resolution as the primary contributor to counter accuracy, timebase errors can also be a major contributor, as shown in Table A-3. For Case 1, the resolution error contributes about 80% of the error. However, for Case 2 the timebase error contributes about 99.9% of the error. For Case 3, triggering is assumed to NOT be at the midpoint of the sine wave, and a slew rate of 1 V/sec is assumed. Note that, for a wide range of conditions, the trigger noise error is not significant.

Table A-3. Effects on Frequency Accuracy of Varying Input Conditions

NOTE Although the combinations shown in Table A-3 do not necessarily reflect actual test conditions, the numbers do indicate that a careful analysis of the input signal and triggering points is required to determine the accuracy of your measurements.

Example: Calculating Period Average Accuracy

For this example, assume the following values/conditions: Input period: 200 µsec (5 kHz square wave) Number periods averaged: 32 Timebase: 6.0×10^{-6} Source Noise (en): 1 mV rms Slew rate: 1000 V/sec Triggering at midpoint of the signal **NOTE** The source noise of 1 mV is a typical value. You will need to measure the noise of your source for most accurate calculations. You can use this example for time interval and pulse width measurements by substituting the appropriate resolution and trigger noise error equations shown in Table A-1.

Period Measurement Accuracy Equation

For period measurements, **accuracy** (sec) $= \pm$ [resolution + timebase error + trigger noise error].

Calculate Resolution

For 32 periods to be averaged, **resolution** $= \pm (200/nper)$ $= \pm (200/32) = \pm 6.250$ nsec

Calculate Timebase Error

For period measurements, **timebase error (sec)** = timebase x period = \pm [initial accuracy + aging rate + temp drift] x period. For this example, timebase error = $\pm (6.0 \times 10^{-6}) \times (200 \times 10^{-6}) = \pm 1.2$ nsec

Calculate Trigger Noise Error

From Table A-1, **trigger noise error (sec)** = 1.4 x T/*nper*, where:

For a 1 mV rms input noise, 32 periods averaged, and a slew rate of 1000 V/sec:

trigger noise error (sec) =

$$
\pm 1.4 \times \sqrt{\frac{(200 \,\mu \,V)^2 + (1000 \,\mu \,V)^2}{32 \times 1000 \,V_{\text{sec}}}} = \pm 43.75 \,\text{nsec}
$$

Calculate Period Average Measurement Accuracy

accuracy (period average measurements)

- $= \pm$ [resolution + timebase error + trig noise error]
- $= \pm (6.250 \text{ nsec} + 1.2 \text{ nsec} + 43.75 \text{ nsec}) = \pm 51.2 \text{ nsec}$

Effects of Varying Signal Conditions

Although this example showed trigger noise error as the primary contributor to counter accuracy, resolution errors and timebase errors can also be major contributors, as shown in Table A-4. For Case 1, the resolution error contributes about 94% of the error. However, for Case 2 the timebase error contributes about 82% of the error. For Case 3, triggering is assumed to NOT be at the midpoint of the sine wave. With a slew rate of 100 V/sec assumed, the trigger noise error contributes about 98.5% of the total error.

Counter Accuracy Programs

Two programs follow to calculate counter accuracies. After you enter the parameter values, the program computes frequency or period/pulse width/time interval measurement accuracies and prints the results. The programs are designed for HP 9000 Series 200/300 computers using BASIC.

Frequency Measurement Accuracy

To make frequency measurement accuracy calculations, first enter the desired number of accuracy calculations you want to make (up to 100 sets of calculations). Then, for each calculation enter the desired values for:

- Frequency (Hz)
- Gate time (sec)
- Timebase
- Source noise $(\mu V \text{ rms})$
- Slew rate $(\mu V/sec)$

The program calculates frequency measurement accuracy for each set of input values and prints or displays the results. A typical display follows the program listing.

NOTE If you want to make more than 100 calculations, change the DIM statement (line 30) for the number of calculations required. Also, if your printer address is not 701, change line 20, PRINTER IS 701 to your printer address. If you do not have a printer, change line 20 to PRINTER IS 1.

Program Listing

Typical display

Frequency Measurement Accuracy (Hz)

Period Measurements Accuracy

To make period/pulse width/time interval accuracy calculations, enter the number of accuracy calculations you want to make (up to 100 sets of calculations). Then, for each calculation enter the desired values for:

- Period/pulse width/time interval (sec)
- Number of periods averaged
- Timebase
- Source noise $(\mu V \text{ rms})$
- Slew rate $(\mu V/sec)$

For pulse width and time interval calculations, enter 1 for the number of periods averaged. The program calculates period, pulse width, or time interval measurement accuracy for each set of input values and displays the results. A typical display follows the program listing.

Typical Display

Period/Pulse Width/Time Interval Measurement Accuracy (sec)

